



**UNIVERSITY COLLEGE OF SCIENCE AND TECHNOLOGY
INSTITUTE OF RADIO PHYSICS & ELECTRONICS**

Date: 14-10-2025

ADVERTISEMENT for WALK-In Interview

Interested candidates are requested to appear in Walk-in-Interview for the following temporary posts in the Dept. of Radio Physics and Electronics, University of Calcutta under the Chips to Startup (C2S) programme funded by Ministry of Electronics and Information Technology (MeitY), Government of India. The detailed Information is as per below:

Sr. No.	Head	Description	
1.	Date and Time of Interview	Date: 6 th November 2025 Time: 3. 00 pm.	
2.	Venue	CAS Building (J.N.Bhar Bhawan), Radio Physics and Electronics, 1, Girish Vidyaratna Lane, Kolkata 700 009	
1	Project Title	Development of an Ultra-Low Power Analog Front-End Circuit and Machine Learning Assisted Processing Unit for Assessment of Air Filter Performance in an IAQM System	
2	Sponsoring Agency	Ministry of Electronics and Information Technology (MeitY), Government of India, vide Ref No.: EE-9/2/2021-R&D-E	
3	Temporary Positions	Project Associate -I: 01 Project Associate -II: 01	
4	Fellowship	Project Associate -I: Rs. 40,000/- per month + 10% increment per annum. Project Associate -II: Rs. 45,000/- per month + 10% increment per annum.	
5	Project Investigators	Chief Investigator: Dr. Soumya Pandit, Associate Professor Co-Chief Investigator: Dr. Arpita Das, Assistant Professor	
6	Department	Department of Radio Physics and Electronics	
7	Tenure of the Assignment	Initially for one year (may be extended up to completion of project based on the performance and availability of fund.)	
8	Essential Qualification	Project Associate-I	B.E./B.Tech. in Engineering/Technology in Electronics and Communication Engineering or Electrical Engineering or other relevant discipline with good academic record. Candidates with M. Sc. in Electronic Science may also apply.
		Project Associate-II	B.E./B.Tech. in Engineering/Technology in Electronics and Communication Engineering or Electrical Engineering or other relevant discipline with good academic record and 2

		Years' working experience in R&D in industrial/academic institution. OR B.E./B.Tech. in Engineering/Technology in Electronics and Communication Engineering or Electrical Engineering or other relevant discipline with good academic record <u>and</u> M.E./M.Tech. in the fields of Electronics and Communication/VLSI Design/or related discipline <u>along</u> with a qualified GATE/CSIR/UGC NET score.
	Desirable Qualification	<ul style="list-style-type: none"> - For Project Associate-I, Candidates having M.E./M.Tech. in the fields of Electronics and Communication/VLSI Design/or related discipline. - Knowledge of VLSI CAD tools (Cadence Virtuoso, Siemens Mentor Graphics, Synopsys, Xilinx Vivado Design Suite, FPGA boards like Zynq, Pynq etc), - Analog IC Design, Layout design, Python/MATLAB. - Knowledge of Linux-based operating system. - Knowledge of Signal/Image Processing and - Embedded Machine Learning Techniques.
9	Age Limit	35 years at the time of application. (5 years' relaxation for SC/ST/OBC/Women/Physically Challenged candidates)

Interested candidates may send their application with biodata in annexed format and photocopies of certificates by Email to Dr. S. Pandit, Chief Investigator, Department of Radio Physics and Electronics, University of Calcutta. (E-mail: sprpe@caluniv.ac.in) on or before 04-11-2025. The shortlisted candidates will be informed by mail/telephone. Selection will be based on qualification, interview & work experience in the proposed field. No TA/DA will be paid for attending the interview. The department reserves the right to reject any application without assigning any reasons thereof.

Soumya Pandit

**Dr. S. Pandit Chief Investigator,
C2S Programme, University of Calcutta**

Note:

- Candidates, before appearing for the selection process, shall ensure that they are eligible for the position they intend to apply for.
- Candidates applying for the post of Project Associate- I/ Project Associate-II shall apply only through the email: sprpe@caluniv.ac.in on or before 04-11-2025.
- The tenure of the project staff is co-terminus with the project.
- Candidates should bring along with them the original certificates at the time of the selection process for verification.
- The decision of the selection committee is final. Canvassing in any form leads to the rejection of the candidature.
- Selected candidates will not be permitted to claim any regular appointment at any other organisation during this period and need to be present regularly as per the rules and regulations of the University and the Funding Agency. The selected candidates will be required to perform duties as assigned by the Supervisor from time to time, depending on the constraints and responsibilities imposed by the Funding Agency.
- The Institute reserves the right not to fill up the posts, cancel the advertisement in whole or in part, without assigning any reason, and the decision of the Administration in this regard shall be final.
- Candidates selected for this position (Project Associate-II) may apply for the regular PhD program (for Project Associate-I, may apply for M. Tech in VLSI Design for Working Professionals) in the department, provided they satisfy the eligibility criterion of the same in the Dept. of Radio Physics and Electronics, University of Calcutta.

UNIVERSITY



OF CALCUTTA

INSTITUTE OF RADIO PHYSICS & ELECTRONICS

Bio Data Format for Recruitment of Project Associate-I/Project Associate-II

Name :
Gender :
Full Address :
Phone :
Mobile :
E-mail: :
Date of Birth :
Category :

Academic Qualifications

Sr. No.	Degree	Specialization	University	Year of Passing	% of Marks/CGPA

Work experience (if any):

Gate/CSIR-UGC NET or other National Level Examination:

Certificates/scholarships/achievements:

Title of B.Tech. Thesis:

Title of M.Tech. Thesis:

Publications (if any):

Conferences/workshops attended:

Expertise and Skill set in VLSI/EDA:

Place:

Date:

Signature of Applicant