

ELECTRONICS ENGINEERING COMPLETE REVISION BATCH
--

S.no.	DATE	DAY	TIME	SUBJECT
Day 1	4 Feb	TUESDAY	9:00AM-12:30PM	DIGITAL ELECTRONICS
Day 2	5 Feb	WEDNESDAY	9:00AM-12:30PM	E.D.C
Day 3	6 Feb	THURSDAY	9:00AM-12:30PM	E.D.C
Day 4	7 Feb	FRIDAY	9:00AM-12:30PM	COMMUNICATIONS
Day 5	8 Feb	SATURDAY	9:00AM-12:30PM	COMMUNICATIONS
Day 6	9 Feb	SUNDAY	9:00AM-12:30PM	MEASUREMENT
Day 7	10 Feb	MONDAY	9:00AM-12:30PM	COMPUTER ENGINEERING
Day 8	11 Feb	TUESDAY	9:00AM-12:30PM	SIGNAL SYSTEM
Day 9	12 Feb	WEDNESDAY	9:00AM-12:30PM	ANALOG ELECTRONICS
Day 10	13 Feb	THURSDAY	9:00AM-12:30PM	NETWORKS
Day 11	14 Feb	FRIDAY	9:00AM-12:30PM	NETWORKS
Day 12	15 Feb	SATURDAY	9:00AM-12:30PM	CONTROL SYSTEM
Day 13	16 Feb	SUNDAY	9:00AM-12:30PM	EMT