

**DRDO Scientist B (EC) By Renu Sir**

	<b>DAY</b>	<b>Tech-1</b>	
4 September	Sunday	On Leave	On Leave
5 September	Monday	On Leave	On Leave
6 September	Tuesday	Weekly Off	Weekly Off
7 September	Wednesday	Analog and Digital Communication Systems	6:00 PM to 8:00 PM
8 September	Thursday	Analog and Digital Communication Systems	6:00 PM to 8:00 PM
9 September	Friday	Analog and Digital Communication Systems	6:00 PM to 8:00 PM
10 September	Saturday	Analog and Digital Communication Systems	3:00 PM to 5:00 PM
11 September	Sunday	Analog and Digital Communication Systems	3:00 PM to 5:00 PM
12 September	Monday	On Leave	On Leave
13 September	Tuesday	Weekly Off	Weekly Off
14 September	Wednesday	On Leave	On Leave
15 September	Thursday	Analog and Digital Circuits	6:00 PM to 8:00 PM
16 September	Friday	Analog and Digital Circuits	6:00 PM to 8:00 PM
17 September	Saturday	Analog and Digital Circuits	3:00 PM to 5:00 PM
18 September	Sunday	Analog and Digital Circuits	3:00 PM to 5:00 PM
19 September	Monday	Analog and Digital Circuits	6:00 PM to 8:00 PM
20 September	Tuesday	Weekly Off	Weekly Off
21 September	Wednesday	Advanced Electronics Topics	6:00 PM to 8:00 PM
22 September	Thursday	Advanced Electronics Topics	6:00 PM to 8:00 PM
23 September	Friday	Advanced Electronics Topics	6:00 PM to 8:00 PM
24 September	Saturday	Advanced Electronics Topics	3:00 PM to 5:00 PM
25 September	Sunday	On Leave	On Leave
26 September	Monday	Advanced Communication Topics	6:00 PM to 8:00 PM
27 September	Tuesday	Advanced Communication Topics	6:00 PM to 8:00 PM
28 September	Wednesday	Advanced Communication Topics	6:00 PM to 8:00 PM

<b>29 September</b>	<b>Thursday</b>	<b>Advanced Communication Topics</b>	<b>6:00 PM to 8:00 PM</b>
<b>30 September</b>	<b>Friday</b>	<b>Syllabus Over</b>	
<b>1 October</b>	<b>Saturday</b>		
<b>2 October</b>	<b>Sunday</b>		
<b>3 October</b>	<b>Monday</b>		
<b>4 October</b>	<b>Tuesday</b>		
<b>5 October</b>	<b>Wednesday</b>		
<b>6 October</b>	<b>Thursday</b>		