CUET PG MCA Computer Science :QUANTUM ULTIMATE Batch Planner FACULTY : Mayank Garg					
Unit	Chapters	Date	Timings		
Orientation Day		Wednesday, August 6, 2025	3:00 - 4:00 PM		
	Main functions of operating systems	Thursday, August 7, 2025	2:00 - 3:00 PM		
	Processes	Friday, August 8, 2025	2:00 - 3:00 PM		
	Threads	Monday, August 11, 2025	2:00 - 3:00 PM		
	Interprocess communication	Tuesday, August 12, 2025	2:00 - 3:00 PM		
	concurrency	Wednesday, August 13, 2025	2:00 - 3:00 PM		
	Synchronization	Thursday, August 14, 2025	2:00 - 3:00 PM		
	Practice Session	Saturday, August 16, 2025	2:00 - 3:00 PM		
	Deadlock	Monday, August 18, 2025	2:00 - 3:00 PM		
	CPU scheduling	Tuesday, August 19, 2025	2:00 - 3:00 PM		
	I/O scheduling	Wednesday, August 20, 2025	2:00 - 3:00 PM		
	Resource scheduling	Thursday, August 21, 2025	2:00 - 3:00 PM		
	Deadlock	Friday, August 22, 2025	2:00 - 3:00 PM		
	Practice Session	Saturday, August 23, 2025	2:00 - 3:00 PM		
Operating System:	scheduling algorithms	Monday, August 25, 2025	2:00 - 3:00 PM		
	banker's algorithm for deadlock handling	Tuesday, August 26, 2025	2:00 - 3:00 PM		
	Memory management and virtual memory	Wednesday, August 27, 2025	2:00 - 3:00 PM		
	Memory management and virtual memory	Thursday, August 28, 2025	2:00 - 3:00 PM		
	File Systems	Friday, August 29, 2025	2:00 - 3:00 PM		
	Practice Session	Saturday, August 30, 2025	2:00 - 3:00 PM		
	I/O systems	Monday, September 1, 2025	2:00 - 3:00 PM		
	DOS	Tuesday, September 2, 2025	2:00 - 3:00 PM		
	DOS	Wednesday, September 3, 2025	2:00 - 3:00 PM		
	UNIX	Thursday, September 4, 2025	2:00 - 3:00 PM		
	Windows	Friday, September 5, 2025	2:00 - 3:00 PM		
	Practice Session	Saturday, September 6, 2025	2:00 - 3:00 PM		
	Doubt Class	Monday, September 8, 2025	2:00 - 3:00 PM		
	Data Structure	Tuesday, September 9, 2025	2:00 - 3:00 PM		
	Arrays and their Applications	Wednesday, September 10, 2025	2:00 - 3:00 PM		
	Sparse Matrix	Thursday, September 11, 2025	2:00 - 3:00 PM		
	Stacks	Friday, September 12, 2025	2:00 - 3:00 PM		
	Queues - Priority Queues	Saturday, September 13, 2025	2:00 - 3:00 PM		
	Linked Lists,	Monday, September 15, 2025	2:00 - 3:00 PM		
	Trees - Forest	Tuesday, September 16, 2025	2:00 - 3:00 PM		
	Practice Session	Wednesday, September 17, 2025	2:00 - 3:00 PM		
	Binary Tree	Thursday, September 18, 2025	2:00 - 3:00 PM		
Data Structura	Threaded Binary Tree	Friday, September 19, 2025	2:00 - 3:00 PM		
	Binary Search Tree	Saturday, September 20, 2025	2:00 - 3:00 PM		
	AVL Tree	Monday, September 22, 2025	2:00 - 3:00 PM		
	B Tree	Tuesday, September 23, 2025	2:00 - 3:00 PM		
Data Structure	Practice Session	Wednesday, September 24, 2025	2:00 - 3:00 PM		
	B+ Tree	Thursday, September 25, 2025	2:00 - 3:00 PM		
	B* Tree	Friday, September 26, 2025	2:00 - 3:00 PM		
	Data Structure for Sets	Saturday, September 27, 2025	2:00 - 3:00 PM		
	Sorting Algorithms	Monday, September 29, 2025	2:00 - 3:00 PM		

Searching Algorithms			l	I
Graphs		Searching Algorithms	Tuesday, September 30, 2025	2:00 - 3:00 PM
Hashing Saturday, October 4, 2025 2:00 - 3:00 PM			-	
Functions Monday, October 6, 2025 2:00 - 3:00 PM				
Recursion		Hashing	Saturday, October 4, 2025	2:00 - 3:00 PM
Parameter Passing		Functions	Monday, October 6, 2025	2:00 - 3:00 PM
Revision Class		Recursion	Tuesday, October 7, 2025	2:00 - 3:00 PM
Doubt Class		Parameter Passing	Wednesday, October 8, 2025	2:00 - 3:00 PM
Digital Fundamentals: Data Types Saturday, October 11, 2025 2:00 - 3:00 PM		Revision Class	Thursday, October 9, 2025	2:00 - 3:00 PM
Number Systems and Conversion Monday, October 13, 2025 2:00 - 3:00 PM		Doubt Class	Friday, October 10, 2025	2:00 - 3:00 PM
Number Systems and Conversion, Complements		Digital Fundamentals: Data Types	Saturday, October 11, 2025	2:00 - 3:00 PM
Complements Tuesday, October 14, 2025 2:00 - 3:00 PM		Number Systems and Conversion	Monday, October 13, 2025	2:00 - 3:00 PM
Floating Point Representation,	Digital Fundamentals		Tuesday, October 14, 2025	2:00 - 3:00 PM
Error Detection Codes		Fixed Point Representation	Wednesday, October 15, 2025	2:00 - 3:00 PM
Computer Arithmetic - Addition, Subtraction, Multiplication and Division Algorithms Practice Session Digital Computers Evaluate Properties Digital Computers Digital Computers Digital Computers Evaluate Properties Evaluate Properties Digital Computers Digital Computers Evaluate Properties Evaluate Properties Digital Computers Evaluate Pricator Properties Evaluate Properties Digital Computers Evaluate Pricator Properties Evaluate Properties Evaluate Properties Digital Computers Evaluate Pricator Properties Evaluate Properties Evaluate Properties Digital Computers Evaluate Pricator Properties Evaluate Propert		Floating Point Representation,	Thursday, October 16, 2025	2:00 - 3:00 PM
Multiplication and Division Algorithms		Error Detection Codes	Friday, October 17, 2025	2:00 - 3:00 PM
Digital Computers			Saturday, October 18, 2025	2:00 - 3:00 PM
Logic Gates Saturday, October 25, 2025 2:00 - 3:00 PM		Practice Session	Thursday, October 23, 2025	2:00 - 3:00 PM
Boolean Algebra, Monday, October 27, 2025 2:00 - 3:00 PM		Digital Computers	Friday, October 24, 2025	2:00 - 3:00 PM
Digital Fundamentals Map Simplifications Tuesday, October 28, 2025 2:00 - 3:00 PM Combinational Circuits Wednesday, October 29, 2025 2:00 - 3:00 PM Practice Session Thursday, October 30, 2025 2:00 - 3:00 PM Flip-Flops Friday, October 31, 2025 2:00 - 3:00 PM Sequential Circuits Saturday, November 1, 2025 2:00 - 3:00 PM Integrated Circuits Monday, November 3, 2025 2:00 - 3:00 PM Decoders Tuesday, November 4, 2025 2:00 - 3:00 PM Multiplexers, Thursday, November 6, 2025 2:00 - 3:00 PM Practice Session Friday, November 7, 2025 2:00 - 3:00 PM Registers Saturday, November 8, 2025 2:00 - 3:00 PM Counters, Monday, November 10, 2025 2:00 - 3:00 PM Memory Unit. Tuesday, November 11, 2025 2:00 - 3:00 PM Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM		Logic Gates	Saturday, October 25, 2025	2:00 - 3:00 PM
Combinational Circuits Practice Session Thursday, October 30, 2025 2:00 - 3:00 PM Flip-Flops Friday, October 31, 2025 2:00 - 3:00 PM Sequential Circuits Saturday, November 1, 2025 2:00 - 3:00 PM Integrated Circuits Monday, November 3, 2025 2:00 - 3:00 PM Decoders Tuesday, November 4, 2025 2:00 - 3:00 PM Multiplexers, Thursday, November 6, 2025 2:00 - 3:00 PM Practice Session Friday, November 7, 2025 2:00 - 3:00 PM Registers Saturday, November 7, 2025 2:00 - 3:00 PM Registers Saturday, November 8, 2025 2:00 - 3:00 PM Counters, Monday, November 10, 2025 2:00 - 3:00 PM Memory Unit. Tuesday, November 11, 2025 2:00 - 3:00 PM Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM		Boolean Algebra,	Monday, October 27, 2025	2:00 - 3:00 PM
Combinational Circuits Wednesday, October 29, 2025 2:00 - 3:00 PM Practice Session Thursday, October 30, 2025 2:00 - 3:00 PM Flip-Flops Friday, October 31, 2025 2:00 - 3:00 PM Sequential Circuits Saturday, November 1, 2025 2:00 - 3:00 PM Integrated Circuits Monday, November 3, 2025 2:00 - 3:00 PM Decoders Tuesday, November 4, 2025 2:00 - 3:00 PM Multiplexers, Thursday, November 6, 2025 2:00 - 3:00 PM Practice Session Friday, November 7, 2025 2:00 - 3:00 PM Registers Saturday, November 8, 2025 2:00 - 3:00 PM Counters, Monday, November 8, 2025 2:00 - 3:00 PM Tuesday, November 10, 2025 2:00 - 3:00 PM Memory Unit. Tuesday, November 11, 2025 2:00 - 3:00 PM Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM		Map Simplifications	Tuesday, October 28, 2025	2:00 - 3:00 PM
Flip-Flops Friday, October 31, 2025 2:00 - 3:00 PM Sequential Circuits Saturday, November 1, 2025 2:00 - 3:00 PM Integrated Circuits Monday, November 3, 2025 2:00 - 3:00 PM Decoders Tuesday, November 4, 2025 2:00 - 3:00 PM Multiplexers, Thursday, November 6, 2025 2:00 - 3:00 PM Practice Session Friday, November 7, 2025 2:00 - 3:00 PM Registers Saturday, November 8, 2025 2:00 - 3:00 PM Counters, Monday, November 10, 2025 2:00 - 3:00 PM Memory Unit. Tuesday, November 11, 2025 2:00 - 3:00 PM Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM		Combinational Circuits	Wednesday, October 29, 2025	2:00 - 3:00 PM
Sequential Circuits Saturday, November 1, 2025 2:00 - 3:00 PM Integrated Circuits Monday, November 3, 2025 2:00 - 3:00 PM Decoders Tuesday, November 4, 2025 2:00 - 3:00 PM Multiplexers, Thursday, November 6, 2025 2:00 - 3:00 PM Practice Session Friday, November 7, 2025 2:00 - 3:00 PM Registers Saturday, November 8, 2025 2:00 - 3:00 PM Counters, Monday, November 10, 2025 2:00 - 3:00 PM Memory Unit. Tuesday, November 11, 2025 2:00 - 3:00 PM Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM		Practice Session	Thursday, October 30, 2025	2:00 - 3:00 PM
Integrated Circuits Monday, November 3, 2025 2:00 - 3:00 PM Decoders Tuesday, November 4, 2025 2:00 - 3:00 PM Multiplexers, Thursday, November 6, 2025 2:00 - 3:00 PM Practice Session Friday, November 7, 2025 2:00 - 3:00 PM Registers Saturday, November 8, 2025 2:00 - 3:00 PM Counters, Monday, November 10, 2025 2:00 - 3:00 PM Memory Unit. Tuesday, November 11, 2025 2:00 - 3:00 PM Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM		Flip-Flops	Friday, October 31, 2025	2:00 - 3:00 PM
Decoders Tuesday, November 4, 2025 2:00 - 3:00 PM Multiplexers, Thursday, November 6, 2025 2:00 - 3:00 PM Practice Session Friday, November 7, 2025 2:00 - 3:00 PM Registers Saturday, November 8, 2025 2:00 - 3:00 PM Counters, Monday, November 10, 2025 2:00 - 3:00 PM Memory Unit. Tuesday, November 11, 2025 2:00 - 3:00 PM Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM		Sequential Circuits	Saturday, November 1, 2025	2:00 - 3:00 PM
Multiplexers, Thursday, November 6, 2025 2:00 - 3:00 PM Practice Session Friday, November 7, 2025 2:00 - 3:00 PM Registers Saturday, November 8, 2025 2:00 - 3:00 PM Counters, Monday, November 10, 2025 2:00 - 3:00 PM Memory Unit. Tuesday, November 11, 2025 2:00 - 3:00 PM Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM		Integrated Circuits	Monday, November 3, 2025	2:00 - 3:00 PM
Practice Session Friday, November 7, 2025 2:00 - 3:00 PM Registers Saturday, November 8, 2025 2:00 - 3:00 PM Counters, Monday, November 10, 2025 2:00 - 3:00 PM Memory Unit. Tuesday, November 11, 2025 2:00 - 3:00 PM Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM		Decoders	Tuesday, November 4, 2025	2:00 - 3:00 PM
Registers Saturday, November 8, 2025 2:00 - 3:00 PM Counters, Monday, November 10, 2025 2:00 - 3:00 PM Memory Unit. Tuesday, November 11, 2025 2:00 - 3:00 PM Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM		Multiplexers,	Thursday, November 6, 2025	2:00 - 3:00 PM
Counters, Monday, November 10, 2025 2:00 - 3:00 PM Memory Unit. Tuesday, November 11, 2025 2:00 - 3:00 PM Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM		Practice Session	Friday, November 7, 2025	2:00 - 3:00 PM
Memory Unit. Tuesday, November 11, 2025 2:00 - 3:00 PM Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM		Registers	Saturday, November 8, 2025	2:00 - 3:00 PM
Revision Class Wednesday, November 12, 2025 2:00 - 3:00 PM			Monday, November 10, 2025	2:00 - 3:00 PM
		Memory Unit.	Tuesday, November 11, 2025	2:00 - 3:00 PM
Doubt Class Thursday, November 13, 2025 2:00 - 3:00 PM		Revision Class	Wednesday, November 12, 2025	2:00 - 3:00 PM
		Doubt Class	Thursday, November 13, 2025	2:00 - 3:00 PM